

09/915833
WEST Search History

Hide Items

Restore

Clear

Cancel

DATE: Monday, July 11, 2005

INVENTOR NAME SEARCH
 DONE ON PARM ST.JL
 7-11-2005 ✓

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	
<input type="checkbox"/>	L144	multiprocessor and l108	3
<input type="checkbox"/>	L143	multitprocssor and l108	0
		DB=USPT; PLUR=YES; OP=OR	
<input type="checkbox"/>	L142	5305362.pn.	1
<input type="checkbox"/>	L141	5455920.pn.	1
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	
<input type="checkbox"/>	L140	L139.ti.	19
<input type="checkbox"/>	L139	multiprocessor same fifo	233
<input type="checkbox"/>	L138	multiprocessor adj fifo	0
<input type="checkbox"/>	L137	multi\$1processor and L136	14
<input type="checkbox"/>	L136	message adj pipeline\$1	35
<input type="checkbox"/>	L135	l132 and l131	0
<input type="checkbox"/>	L134	multi\$1processor and L133	0
<input type="checkbox"/>	L133	L132.ti.	6
<input type="checkbox"/>	L132	message adj fifo	266
<input type="checkbox"/>	L131	l123 and l129	1
<input type="checkbox"/>	L130	l128 and L129	1
<input type="checkbox"/>	L129	message\$1 same pipeline same fifo	63
<input type="checkbox"/>	L128	multi\$1processor and L126	5286
<input type="checkbox"/>	L127	multi\$1 processor and L126	1101232
<input type="checkbox"/>	L126	shared adj memory	17962
<input type="checkbox"/>	L125	l124 and L123	30
<input type="checkbox"/>	L124	pipeline same fifo	2053
<input type="checkbox"/>	L123	multiprocessor.ti.	7383
		DB=EPAB; PLUR=YES; OP=OR	
<input type="checkbox"/>	L122	EP-1139228-A2.did.	1
		DB=USPT; PLUR=YES; OP=OR	
<input type="checkbox"/>	L121	5724512.pn.	1
<input type="checkbox"/>	L120	5787300.pn.	1
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	

<input type="checkbox"/>	L119 L118.ti.	19
<input type="checkbox"/>	L118 FIFO same multiprocessor\$1	233
<input type="checkbox"/>	L117 message\$1 same FIFO same multiprocessor\$1	26
<input type="checkbox"/>	L116 message\$1 same pipeline same FIFO same multiprocessor\$1	0
<input type="checkbox"/>	L115 multiprocessor\$1 and l104 <i>DB=USPT; PLUR=YES; OP=OR</i>	30
<input type="checkbox"/>	L114 4616338.pn.	1
<input type="checkbox"/>	L113 4694426.pn. <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	1
<input type="checkbox"/>	L112 L108 same message\$1 <i>DB=USPT; PLUR=YES; OP=OR</i>	1
<input type="checkbox"/>	L111 5185599.pn.	1
<input type="checkbox"/>	L110 US-5940601-A.did. <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	1
<input type="checkbox"/>	L109 L108.ti.	1
<input type="checkbox"/>	L108 FIFO adj pipeline\$1	92
<input type="checkbox"/>	L107 messag\$3 adj pipeline\$1 adj FIFO\$1	0
<input type="checkbox"/>	L106 L7 same L104	0
<input type="checkbox"/>	L105 L97 and L104	1
<input type="checkbox"/>	L104 messag\$3 same pipeline\$1 same FIFO\$1	76
<input type="checkbox"/>	L103 4862350.pn.	2
<input type="checkbox"/>	L102 L97 and L101	10
<input type="checkbox"/>	L101 multiprocessor.ti. <i>DB=USPT; PLUR=YES; OP=OR</i>	7383
<input type="checkbox"/>	L100 5838766.pn.	1
<input type="checkbox"/>	L99 6092098.pn.	1
<input type="checkbox"/>	L98 6233630.pn. <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	1
<input type="checkbox"/>	L97 L84 same FIFO	93
<input type="checkbox"/>	L96 FIFO and L95	16
<input type="checkbox"/>	L95 L84 and L94	16
<input type="checkbox"/>	L94 L39 and L46	155
<input type="checkbox"/>	L93 L92.ti.	6
<input type="checkbox"/>	L92 message adj FIFO <i>DB=USPT; PLUR=YES; OP=OR</i>	266
<input type="checkbox"/>	L91 4980852.pn. <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	1
<input type="checkbox"/>	L90 L38 and L89	6

<input type="checkbox"/>	L89 L87 and L26	16
<input type="checkbox"/>	L88 L87 and L37	0
<input type="checkbox"/>	L87 (fifo or queue) same L86	874
<input type="checkbox"/>	L86 L85.ti.	229861
<input type="checkbox"/>	L85 L84 or L39	896723
<input type="checkbox"/>	L84 multiple adj processor\$1	13264
	<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L83 6112286.pn.	1
<input type="checkbox"/>	L82 6003123.pn.	1
<input type="checkbox"/>	L81 5944789.pn.	1
<input type="checkbox"/>	L80 5938765.pn.	1
<input type="checkbox"/>	L79 5892915.pn.	1
<input type="checkbox"/>	L78 5737605.pn.	1
<input type="checkbox"/>	L77 4691280.pn.	1
<input type="checkbox"/>	L76 3921145.pn.	1
<input type="checkbox"/>	L75 4015243.pn.	1
<input type="checkbox"/>	L74 4041461.pn.	1
<input type="checkbox"/>	L73 4073005.pn.	1
<input type="checkbox"/>	L72 4130864.pn.	1
<input type="checkbox"/>	L71 4144563.pn.	1
<input type="checkbox"/>	L70 4313161.pn.	1
<input type="checkbox"/>	L69 4322795.pn.	1
<input type="checkbox"/>	L68 4045781.pn.	1
<input type="checkbox"/>	L67 4345309.pn.	1
<input type="checkbox"/>	L66 4395753.pn.	1
<input type="checkbox"/>	L65 4161024.pn.	1
<input type="checkbox"/>	L64 4096567.pn.	1
<input type="checkbox"/>	L63 4078254.pn.	1
<input type="checkbox"/>	L62 4075686.pn.	1
<input type="checkbox"/>	L61 4056844.pn.	1
<input type="checkbox"/>	L60 3566358.pn.	1
<input type="checkbox"/>	L59 4410944.pn.	1
<input type="checkbox"/>	L58 4445174.pn.	1
<input type="checkbox"/>	L57 6594735.pn.	1
<input type="checkbox"/>	L56 6604182.pn.	1
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L55 L37 and L54	3
<input type="checkbox"/>	L54 L39 and L50	4278

<input type="checkbox"/>	L53 L37 and L51	0
<input type="checkbox"/>	L52 L51.ti.	0
<input type="checkbox"/>	L51 fifo same L50	404
<input type="checkbox"/>	L50 message adj queue\$1	6186
<input type="checkbox"/>	L49 L46.ti.	0
<input type="checkbox"/>	L48 L46 and L39	155
<input type="checkbox"/>	L47 L37 and L46	0
<input type="checkbox"/>	L46 fifo same message adj buffer\$1	174
	<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L45 5949982.pn.	1
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L44 L38 and L43	20
<input type="checkbox"/>	L43 messag\$3 and L41	20
<input type="checkbox"/>	L42 meessag\$3 and L41	0
<input type="checkbox"/>	L41 L38 and L40	47
<input type="checkbox"/>	L40 L37 and L39	136
<input type="checkbox"/>	L39 multiprocessor\$1 or coprocessor\$1 or processors	896723
<input type="checkbox"/>	L38 shared adj memory	17962
<input type="checkbox"/>	L37 (700/5).ccls.	179
<input type="checkbox"/>	L36 700/5	204
<input type="checkbox"/>	L35 L18 and L34	0
<input type="checkbox"/>	L34 L30 and L33	42
<input type="checkbox"/>	L33 queue\$1 or fifo\$1	107849
<input type="checkbox"/>	L32 L30 and L31	0
<input type="checkbox"/>	L31 message adj circuit\$1	775
<input type="checkbox"/>	L30 L28 and L29	67
<input type="checkbox"/>	L29 (microprocessors or processors) same (shared adj memor\$3)	7755
<input type="checkbox"/>	L28 L26 and L27	87
<input type="checkbox"/>	L27 L23 same L24	628
<input type="checkbox"/>	L26 L5 or L13 or L25	11762
<input type="checkbox"/>	L25 (710/100 710/300 710/301 710/302 710/303 710/304 710/104 710/105 710/106 710/107 710/108 710/109 710/110 710/111 710/112 710/113 710/114 710/115 710/116 710/117 710/118 710/119 710/120 710/121 710/122 710/123 710/124 710/125 710/305 710/306 710/307 710/308 710/309 710/310 710/311 710/312 710/313 710/314 710/315 710/316 710/317).ccls.	9407
<input type="checkbox"/>	L24 shared adj memor\$3	17965
<input type="checkbox"/>	L23 system adj bus\$2	55779
<input type="checkbox"/>	L22 address and L21	2

<input type="checkbox"/>	L21	command\$1 and L20	2
<input type="checkbox"/>	L20	fifo and L19	5
<input type="checkbox"/>	L19	(shared adj memory) and L18	20
<input type="checkbox"/>	L18	(multiprocessor same message\$1).ti.	121
<input type="checkbox"/>	L17	L6 and L16	15
<input type="checkbox"/>	L16	pipeline and L15	18
<input type="checkbox"/>	L15	fifo and L14	68
<input type="checkbox"/>	L14	multiprocessor\$1 and L13	68
<input type="checkbox"/>	L13	L8 and L12	162
<input type="checkbox"/>	L12	(711/118 711/119 711/120 711/121 711/122 711/123 711/124 711/125 711/126 711/127 711/128 711/129 711/130 711/131 711/132 711/133 711/134 711/135 711/136 711/137 711/138 711/139 711/140 711/141 711/142 711/143 711/144 711/145 711/146 711/147 711/148 711/149 711/150 711/151 711/152 711/153 711/154 711/155 711/156 711/157 711/158 711/159 711/160 711/161 711/162 711/163 711/164 711/165 711/166 711/167 711/168 711/169).ccls.	15289
<input type="checkbox"/>	L11	L7 and L10	0
<input type="checkbox"/>	L10	circuit same L9	5
<input type="checkbox"/>	L9	pipeline same L8	63
<input type="checkbox"/>	L8	message\$1 same fifo	3347
<input type="checkbox"/>	L7	multiprocessor\$1 same (shared adj memor\$3)	2560
<input type="checkbox"/>	L6	multiprocessor\$1 and (shared adj memor\$3)	4584
<input type="checkbox"/>	L5	L4 or L3 or L2 or L1	2376
		<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L4	(712/246 712/247 712/248).ccls.	252
<input type="checkbox"/>	L3	(712/203).ccls.	107
<input type="checkbox"/>	L2	(712/32 712/33 712/34 712/35 712/36 712/37 712/38 712/39 712/40 712/41 712/42 712/43).ccls.	1588
<input type="checkbox"/>	L1	(719/312 719/313 719/314).ccls.	466

END OF SEARCH HISTORY